



**Product Information** 

**DATE**: 15. Jul. 2011

**SAMSUNG TFT-LCD** 

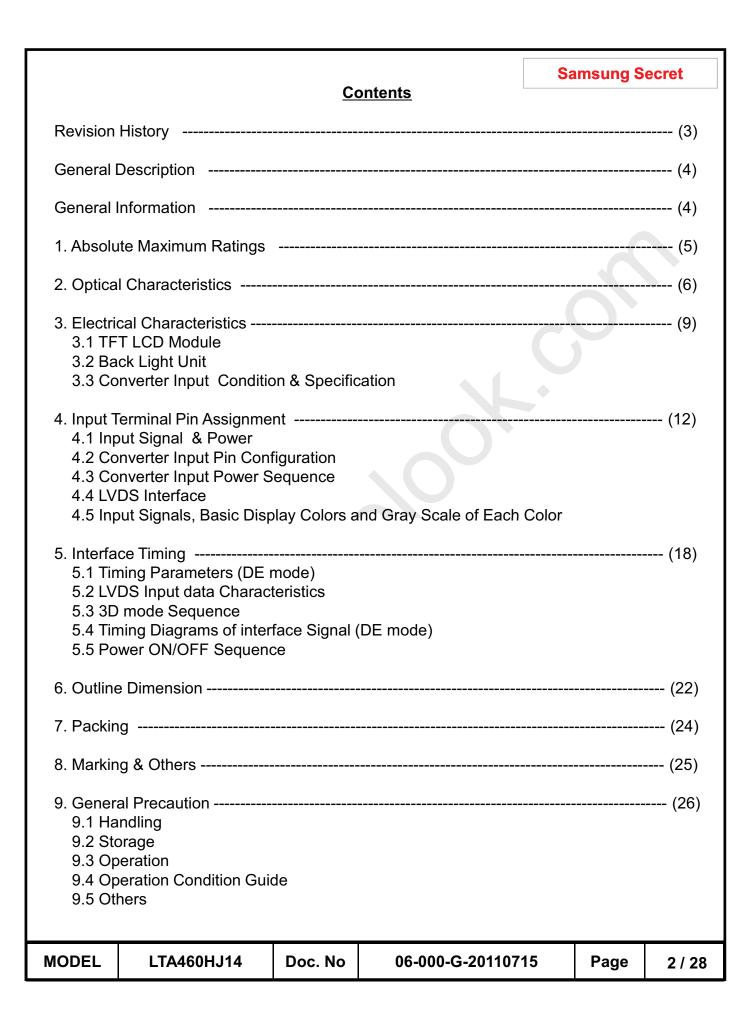
MODEL: LTA460HJ14

The Information Described in this Specification is Preliminary and can be changed without prior notice

**LCD Business** 

Samsung Electronics Co., LTD.

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# **Revision History**

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Date	Rev. No	Page	Summary
15. Jul. 2011	000	all	First issued

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#### **General Description**

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## Description

**LTA460HJ14** is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit.

The resolution of a 46.0" is 1920 x 1080 and this model can display up to 16.7 Million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

#### **Features**

- RoHS compliance (Pb-free)
- High contrast ratio & aperture ratio with wide color gamut
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle (± 178°)
- High speed response
- FHD resolution (16:9)
- Low Power consumption
- Edge Type LED (Light Emitted Diode) BLU
- DE (Data Enable) mode
- 4ch LVDS (Low Voltage Differential Signaling) interface (4 pixel/clock)

#### **General Information**

Items	Specification	Unit	Note
Module Size	1055.5(H <sub>TYP</sub> ) x 610.8 (V <sub>TYP</sub> )	mm	± 1.0mm
Wodule Size	31.1 (D <sub>TYP</sub> )	111111	
Weight	11,800 (Max.)	g	
Pixel Pitch	0.53025(H) x 0.17675(W) * 3	mm	
Active Display Area	1018.08(H) x 572.67(V)	mm	
Surface Treatment	Haze 7%	-	Anti-Glare
Display Colors	8bit – 16.7M	Colors	
Number of Pixels	1920 x 1080	Pixel	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normally Black	-	
Luminance of White	400 (Typ.)	cd/m <sup>2</sup>	

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# 1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V <sub>DD</sub>	GND-0.3	16.5	V	(1)
Storage temperature	T <sub>STG</sub>	-20	60	C	(2)
Operation temperature	T <sub>OPR</sub>	0	50	c	(2)
Surface temperature	T <sub>SUR</sub>	0	60	C	-
Shock ( non - operating )	S <sub>nop</sub>	-	30	G	(3)
Vibration ( non - operating )	$V_{nop}$	-	1.5	G	(4)

Note (1) Ta= 25 ± 2 ℃

- (2) Temperature and relative humidity range are shown in the figure below.
  - a. 90 % RH Max. (Ta ≤ 39 °C)
  - b. Relative Humidity is 90% or less. (Ta > 39 ℃)
  - c. No condensation
- (3) 11ms, sine wave, one time for  $\pm X$ ,  $\pm Y$ ,  $\pm Z$  axis
- (4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

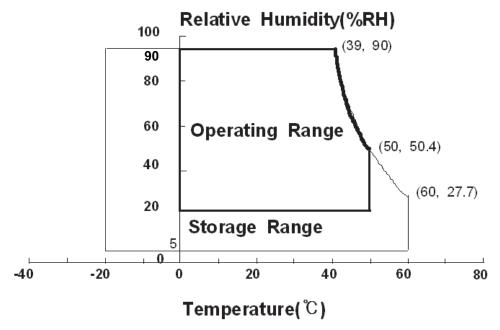


Fig. Temperature and Relative humidity range

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# 2. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent. Measuring equipment: TOPCON RD-80S, TOPCON SR-3, ELDIM EZ-Contrast

(Ta = 25  $\pm$  2°C, VDD=12.0V, fv=120Hz, f<sub>DCLK</sub>=297MHz, Dimming Duty = Max)

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
Contrast I (Center of s		C/R		3,000	4,000	ı		(1) SR-3	
Response Time	G-to-G	Tg		-	8	1	msec	(3) RD-80S BM-7	
Luminance of (Center of s		Y <sub>L</sub>	Normal	300	400	-	cd/m <sup>2</sup>	(4) SR-3	
		Rx	θ <b>L,R</b> =0 θ <b>U,D</b> =0		0.653				
	Red	Ry	,	,		0.330			
	0	Gx	Viewing Angle		0.310				
Color	Green	Gy	1.19.5	TYP.	0.600	TYP.		(5),(6) SR-3	
Chromaticity (CIE 1931)	Blue	Bx		-0.03	0.150	+0.03		PR650	
	blue	Ву			0.058				
	White	Wx			0.280				
	White	Wy			0.290				
Color Ga	mut	-		-	72	1	%	(5)	
Color Temp	erature	ССТ		-	10,000	ı	K	SR-3	
	Hor.	$\theta_L$		75	89	1			
Viewing	HOI.	$\theta_{R}$	C/R≥10	75	89	1	Degree	(6)	
Angle	Ver.	$\theta_{\sf U}$	U/N210	75	89	-	Degree	EZ-Contrast	
	vei.	$\theta_{D}$		75	89	-			
Brightness U		B <sub>uni</sub>		-	-	25	%	(2) SR-3	

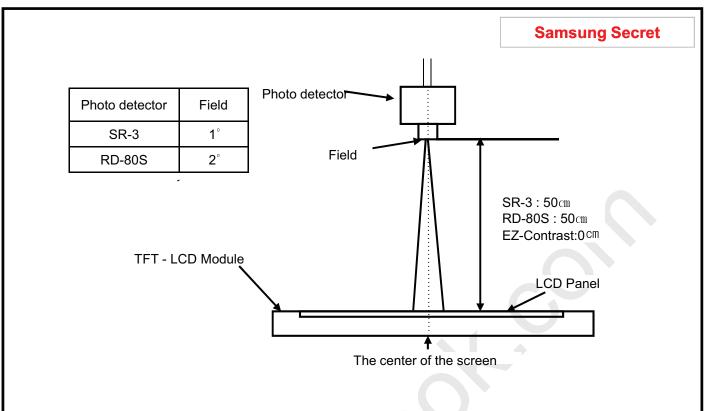
#### - Test Equipment Setup

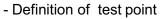
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

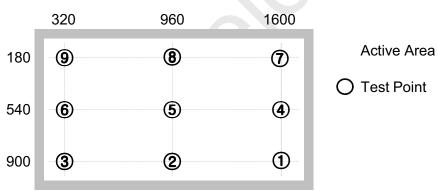
. Dimming Duty = 100%

. Environment condition : Ta = 25 ± 2 ℃

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Note (1) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G \max}{G \min}$$

Gmax: Luminance with all pixels white Gmin: Luminance with all pixels black

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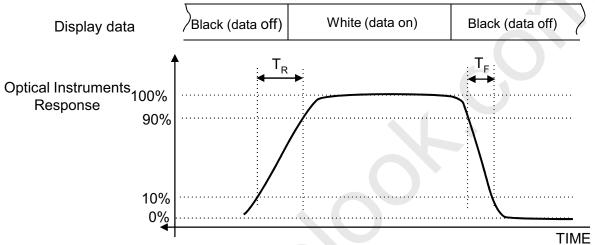
Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$Buni = 100*\frac{(B \max - B \min)}{B \max}$$

Global LCD Panel Exchange Center

Bmax: Maximum brightness Bmin: Minimum brightness

Note (3) Definition of Response time: Sum of Tr, Tf

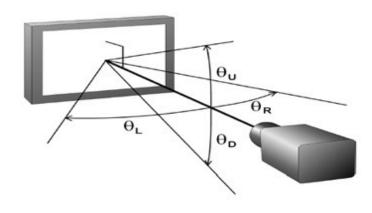


G- to- G: Average response time between Gray to Gray (Scale)

Note (4) Definition of Luminance of White: Luminance of white at center point (5)

Note (5) Definition of Color Chromaticity (CIE 1931) Color coordinate of Red, Green, Blue & White at center point (5)

Note (6) Definition of Viewing Angle : Viewing angle range (C/R ≥10)



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### 3. Electrical Characteristics

#### 3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25 °C ± 2 °C

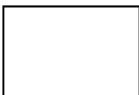
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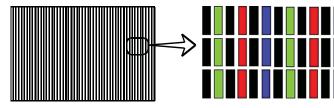
Item		Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of	Power Supply	$V_{DD}$	10.8	12.0	13.2	V	(1)
Current	(a) Black		-	720	950	mA	
of Power	(b) White	I <sub>DD</sub>	-	1280	1500	mA	(2),(3)
Supply	(c) N-pattern		-	1280	1500	mA	
Vsync Free	quency	f <sub>V</sub>	100	120	125	Hz	
Hsync Free	Hsync Frequency		120	135	140	kHz	
Main Frequency		Fdclk	260	297	310	MHz	
Rush Curre	Rush Current		-	5	7	А	(4)

Note (1) The ripple voltage should be controlled under 10% of  $V_{DD}$ .

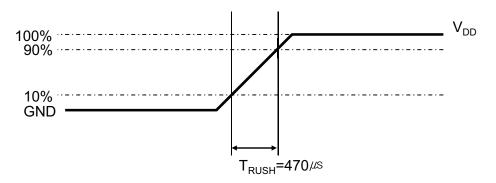
- (2) fv=120Hz, fDCLK=297MHz,  $V_{DD}=12.0V$ , DC Current.
- (3) Power dissipation check pattern (LCD Module only)
- a) Black Pattern
- b) White Pattern







#### (4) Measurement Conditions



Rush Current I<sub>RUSH</sub> can be measured when  $T_{RUSH}$ . is 470 $\mu$ S.

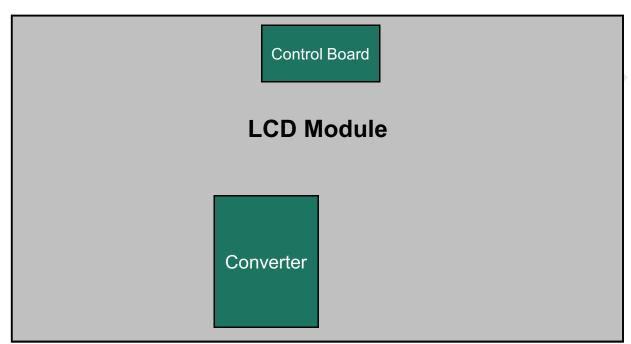
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# 3.2 Back Light Unit

The back light unit contains Edge type White LEDs (Light Emitting Diode)

Ta=25 ± 2℃



Item	Symbol	Min.	Тур.	Max.	Unit	Note
Operating Life Time	Hr	30,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : Ta =  $25\pm2\,^{\circ}$ C, For single LED only. ]

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# 3.3 Converter Input Condition & Specification

## **Samsung Secret**

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Items	Symbol	Conditions	Min.	Тур.	Max.	Unit	Note
Input Voltage	Vin	-	22	24	26	V	Ta=25± 2 ℃
Input Current	_	Vin=24.0V	-	-	3.8	Α	2D Mode
input Current	I <sub>RUSH</sub>	Vdim =3.3V	-	-	TBD	А	3D Mode
Output		Vin = 24.0V	-	140	-	mArms	2D Mode
Current	I <sub>O,MAX</sub>	V dim =3.3 V	-	TBD	-	mArms	3D Mode
Backlight	ON	Vin=24.0 V	2.4	-	5.25	V	
On/Off	OFF	Vin=24.0 V	0	-	0.8		
Dimming Range	V_ <sub>DIM</sub>	Vin :22~26V	0	-	3.3	V	
Dimming Frequency	F <sub>PWM</sub>	Vin=24.0 V	-	170	-	Hz	
External Dimming Duty Range	EX_Dim	Min	1	-	100	%	
External Dimming Frequency Range	F <sub>EX_PWM</sub>	Vin=22.0~26.0 V	120	-	180	Hz	Dim Pin(#13) : Floating
External Dimming	V	High (ON)	2.4	-	5.25	V	
Signal Level	$V_{PWM}$	Low (Off)	0	-	0.8	V	

- Note) Power Consumption is measured when 400 [cd/m ] of luminance which is the typical luminance.
- (1) All data is measured after 120min warm-up.
- Additional Appendix for Supply Current & Power consumption

ITEN 4	CVMDOL	CONDITION	SPI	ECIFICA	TION	UNIT	NOTE
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	NOTE
	lovershoot, 2D		-	TBD	TBD	Adc	Maximum current after turn-on
Innut Current	Isaturation,2D		-	TBD	TBD	Adc	Saturation current after 1hr aging
Input Current	Iovershoot,3D	Vin=24V, dim=max	-	TBD	TBD	Adc	Maximum current after turn-on
	Isaturation,3D		-	TBD	TBD	Adc	Saturation current after 1hr aging

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# 4. Input Terminal Pin Assignment

# **Samsung Secret**

4.1.1 Input Signal & Power

Connector: FI-RE41S-HF (JAE/UJU)

Pin		Description	Pin	Symbol	Description	
1		Vdd(12V)			Rx1[3]P	
2		Vdd(12V)	22		No Connection	
3		Vdd(12V)	23		No Connection	
4		Vdd(12V)	24		GND	
5		Vdd(12V)	25		Rx3[0]N	
6	N	o Connection	26		Rx3[0]P	
7		GND	27		Rx3[1]N	
8	GND GND		28		Rx3[1]P	
9			29	ODD LVDS	Rx3[2]N	
10		Rx1[0]N	30	SIGNAL	Rx3[2]P	
11		Rx1[0]P	31		GND	
12		Rx1[1]N	32		Rx3CLK-	
13		Rx1[1]P	33		Rx3CLK+	
14		Rx1[2]N	34		GND	
15	ODD LVDS SIGNAL	Rx1[2]P	35		Rx3[3]N	
16		GND	36		Rx3[3]P	
17		Rx1CLK-	37		No Connection	
18		Rx1CLK+	38		No Connection	
19		GND	39		GND	
20		Rx1[3]N	40	N	No Connection	
			41	N	No Connection	

Note) No Connection: This PINS are only used for SAMSUNG internal using.

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Connector: FI-RE51S-HF (JAE/UJU)

# 4.1.2 Input Signal & Power

Pin		Description	Pin		Description		
1		Vdd(12V)	26		Rx4[0]P		
2		Vdd(12V)	27		Rx4[1]N		
3		Vdd(12V)	28		Rx4[1]P		
4		Vdd(12V)	29		Rx4[2]N		
5		Vdd(12V)	30		Rx4[2]P		
6		No Connection	31		GND		
7		GND	32	EVEN LVDS SIGNAL	Rx4CLK-		
8		GND	33	OIOI WILL	Rx4CLK+		
9		GND	34		GND		
10		Rx2[0]N	35		Rx4[3]N		
11		Rx2[0]P	36		Rx4[3]P		
12		Rx2[1]N	37		No Connection		
13		Rx2[1]P	38		No Connection		
14		Rx2[2]N	39		GND		
15		Rx2[2]P	40		No Connection		
16		GND	41		No Connection		
17	EVEN	Rx2CLK-	42	3D_EM	3D_EN signal (Note 2)		
18	LVDS	Rx2CLK+	43		No Connection		
19	SIGNAL	GND	44		No Connection		
20		Rx2[3]N	45		No Connection		
21		Rx2[3]P	46		No Connection		
22		No Connection	47		No Connection		
23		No Connection	48	3D_SYNC_I	Shutter glass Sync Input signa (Note 3)		
24		GND	49	3D_SYNC_O	Shutter glass Sync Signal		
25		Rx4[0]N	50		No Connection		
		1	51		No Connection		

Note (1) No Connection: These PINS are used only for SAMSUNG. (DO NOT CONNECT)

Note (2) 3D Enable signal voltage level

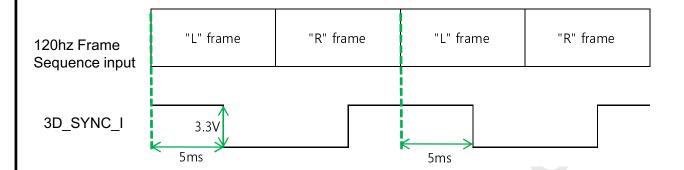
High: Min 2.7V, Max 3.3V Low: Min 0 V, Max 0.4V

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Note3) Recommend timing for 3D\_SYNC\_I Signal.

- Guide Signal to Separate L frame and R frame
- Shutter glass signal & Operation timing also depend on this signal
- To operate 3D function, need this signal from Set A/D board. (In Order for using it in 2D mode, change the input condition into High)



#### Note4) Pin number starts from Left side

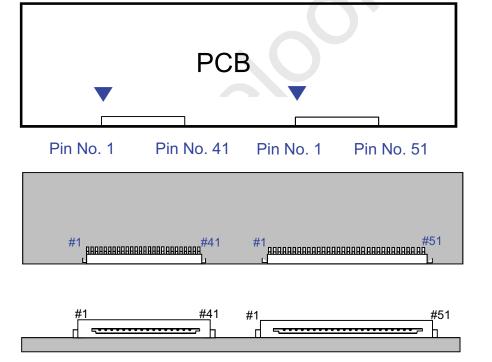


Fig. Connector diagram

- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pins should be separated from other signal or power.

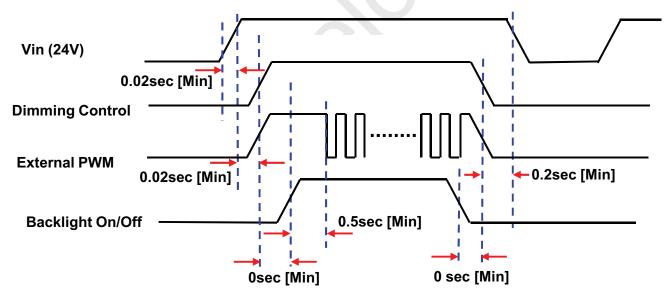


# 4.2 Converter Input Pin Configuration

Connector: Yeon-ho, 20022WR-14B1

Pin No.	Pin Configuration(FUNCTION)
PIII NO.	Master
1 ~ 5	24 V
6 ~ 10	GND
11	No Connection (DO NOT CONNECT)
12	Backlight On /Off [ON:2.4 - 5.25 V, OFF: 0 - 0.8 V]
13	Dimming Control [0V:Min, 3.3V:Max]
14	No Connection (DO NOT CONNECT)

## 4.3. Converter Input Power Sequence



Note) SEQUENCE : ON = Vin(24V) > Dimming Control ≥ Backlight On/Off OFF = Backlight On/Off ≥ Dimming Control > Vin(24V)

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## 4.4 LVDS Interface

- LVDS Receiver : T-con (merged)

- Data Format (JEIDA Only)

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	LVDS pin	JEIDA -DATA		
	TxIN/RxOUT0	R2		
	TxIN/RxOUT1	R3		
	TxIN/RxOUT2	R4		
TxOUT/RxIN0	TxIN/RxOUT3	R5		
	TxIN/RxOUT4	R6		
	TxIN/RxOUT6	R7		
	TxIN/RxOUT7	G2		
	TxIN/RxOUT8	G3		
	TxIN/RxOUT9	G4		
TxOUT/RxIN1	TxIN/RxOUT12	G5		
	TxIN/RxOUT13	G6		
	TxIN/RxOUT14	G7		
	TxIN/RxOUT15	B2		
	TxIN/RxOUT18	В3		
	TxIN/RxOUT19	B4		
	TxIN/RxOUT20	B5		
	TxIN/RxOUT21	В6		
TxOUT/RxIN2	TxIN/RxOUT22	B7		
	TxIN/RxOUT24	HSYNC		
	TxIN/RxOUT25	VSYNC		
	TxIN/RxOUT26	DEN		
	TxIN/RxOUT27	R0		
	TxIN/RxOUT5	R1		
	TxIN/RxOUT10	G0		
TxOUT/RxIN3	TxIN/RxOUT11	G1		
	TxIN/RxOUT16	В0		
	TxIN/RxOUT17	B1		
	TxIN/RxOUT23	RESERVED		
	<u> </u>	<u> </u>		
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# 4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

												D	ATA S	SIGN	٩L											GRAY
COLOR	DISPLAY (8bit)				RE	ED							GRE	EEN							BL	UE				SCALE
	, ,	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	B1	B2	В3	B4	B5	В6	В7	LEVEL
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
BASIC	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
COLOR	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE	1	:	:	:	:	:	:			:	:	:	:	:	:				:	:	:	:	:			R3~
OF RED	<b>↓</b>	:	:	:	:	:	:			:	:	:	:	:	: (			:	:	:	:	:	:			R 252
	LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R 253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R 254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
	DARK	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE	1			:	:		:			:		:	:					:	:		:	:	:			G3~
OF GREEN	.1.				:	:						:	:					:	:		:	:	:			G252
	LIĞHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	В0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
05.11	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
GRAY SCALE	1	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~
OF BLUE	<b>↓</b>	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B252
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note) Definition of Gray:

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal: 0 = Low level voltage, 1 = High level voltage

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# 5. Interface Timing

#### **Samsung Secret**

# 5.1 Timing Parameters ( DE mode )

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock		1/T <sub>C</sub>	260	297	305	MHz	-
Hsync	Frequency	F <sub>H</sub>	120	135	140	KHz	-
Vsync		F <sub>V</sub>	100	120	125	Hz	-
Vertical Display Term Horizontal Display Term	Active Display Period	T <sub>VD</sub>	-	1080	-	Lines	-
	Vertical Total	T <sub>V</sub>	1092	1125	1380	Lines	-
	Active Display Period	T <sub>HD</sub>	-	1920	-	Clocks	-
	Horizontal Total	T <sub>H</sub>	2092	2200	2348	clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

- (1) Test Point: TTL control signal and CLK at LVDS Tx input terminal in system
- (2) Internal V<sub>DD</sub> = 3.3 V
- (3) Spread spectrum
  - Modulation rate (max): ± 1.5 %
  - Modulation Frequency: under 100 KHz

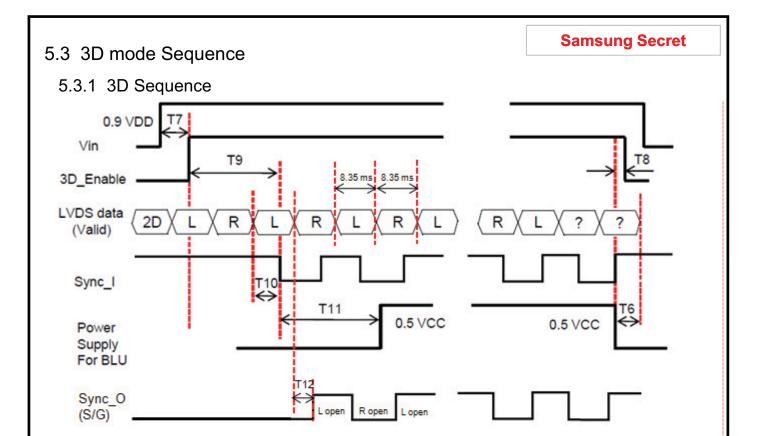
#### 5.2 LVDS Input Data Characteristics

ITI	EM	SYMBOL	Min.	Тур.	Max.	UNIT	NOTE
Input Data Position	F <sub>IN</sub> =85MHz		-	-	400	ps	
	F <sub>IN</sub> =78MHz	$t_{RSRM}$	-	-	450	ps	
	F <sub>IN</sub> =74.25MHz		-	-	500	ps	
Input Data Position	F <sub>IN</sub> =85MHz		-400	-	-	ps	-
	F <sub>IN</sub> =78MHz	$t_{RSLM}$	-450	-	-	ps	
	F <sub>IN</sub> =74.25MHz		-500	-	-	ps	
Input common mode voltage		$V_{CM}$	0.3	-	1.8	V	-
Differential I	Differential Input Voltage		200	350	600	mV	-

Note) When the skew is measured the Spread Spectrum should be 0%

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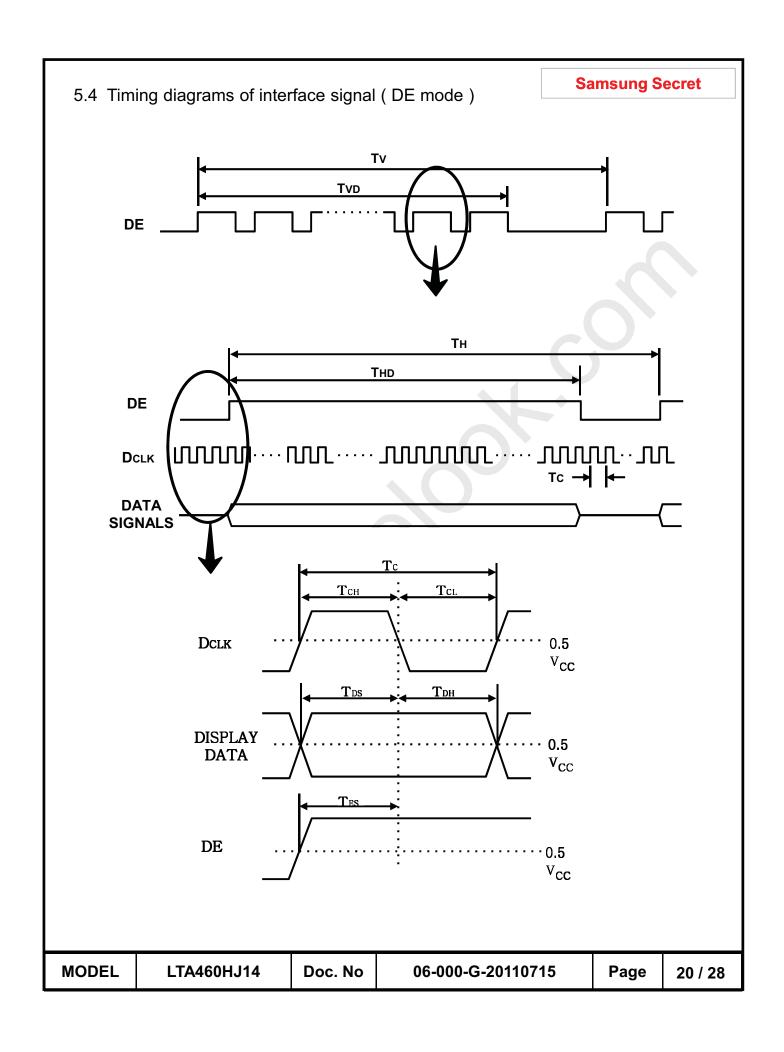
	spec	Measure	Result		spec	Measure	Result
T5	≥ 1000 msec			Т8	> 0 msec		
T6	≥ 100 msec			Т9	> 0 msec		
T7	≥ 2 sec			T10	Typ. 5 msec		
T12	Typ. 4.5msec				***		

\* T10 : Sync\_I is checked with Valid Active L frame

# 5.3.2 Level of 3D Control signal

Test Items	Test Condition		{	Spec
restitems	Test Condition		Min	Max
2D Enable Level	C-PBA Input Level	High	2.7	3.3
3D Enable Level	(Change to 3D mode)	Low	0.0	0.4
2D CVNC I	C-PBA Input Level	High	2.7	3.3
3D_SYNC_I	(L/R Sync)	Low	0.0	0.4
2D CVNC O	Shutter Glasses Sync	High	2.7	3.3
3D_SYNC_O	Level	Low	0.0	0.4

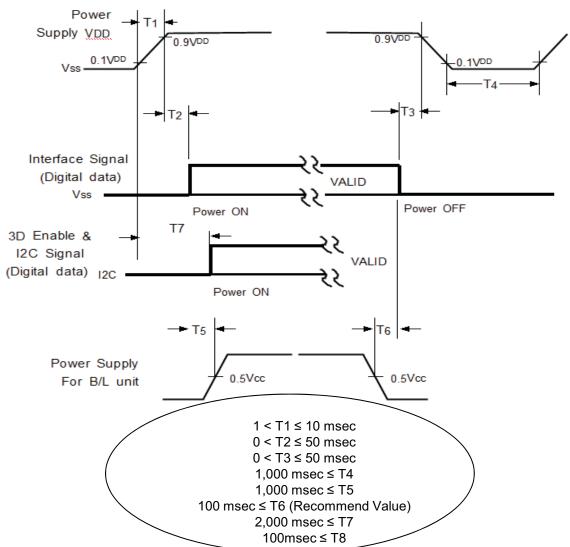
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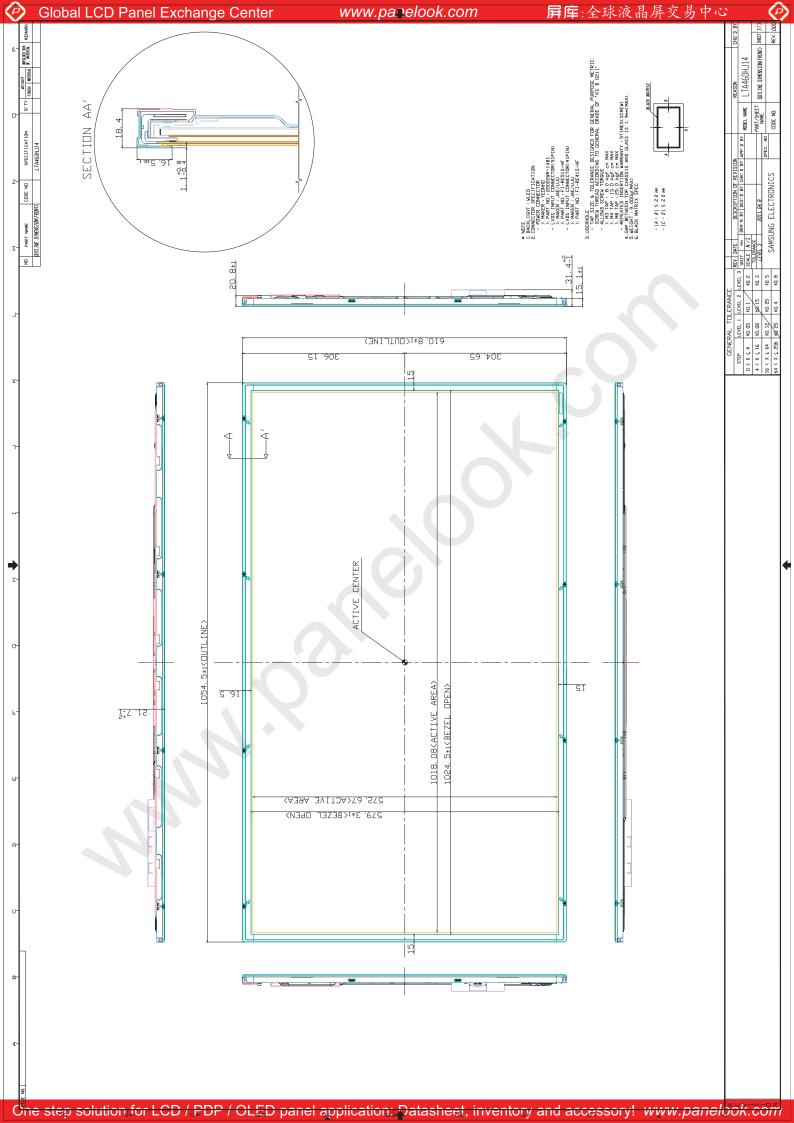
## 5.5 Power ON/OFF Sequence

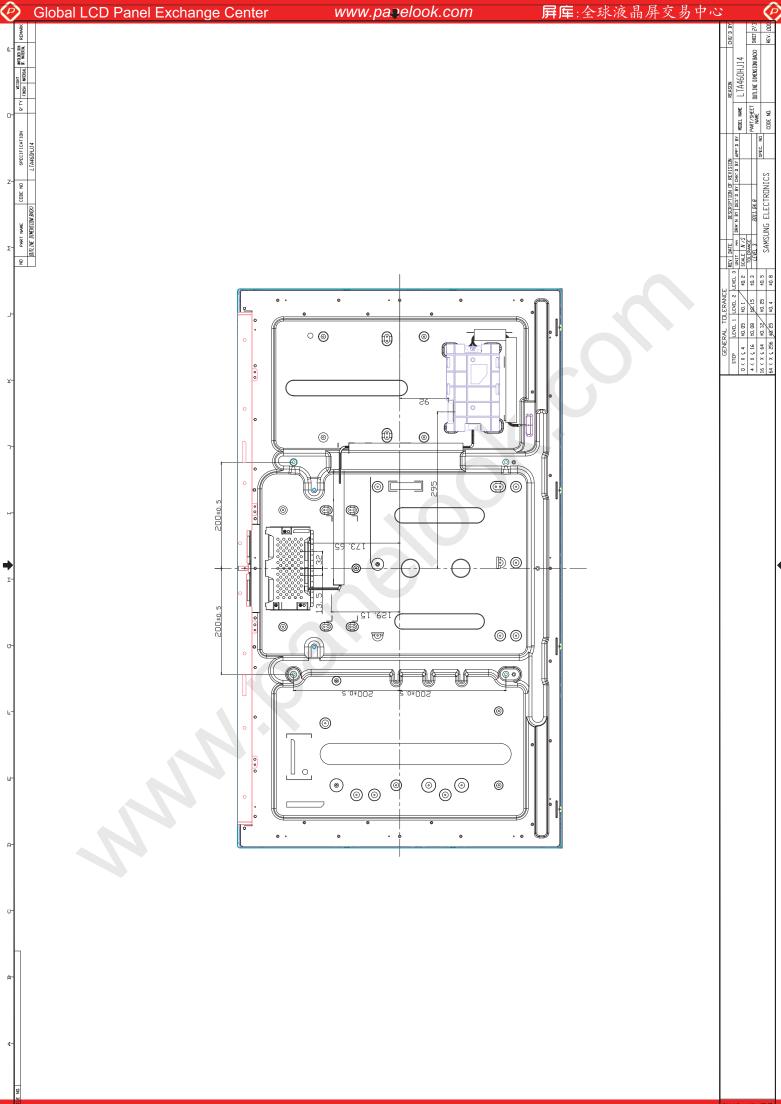
To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



- The supply voltage of the external system for the Module input should be the same as the definition of V<sub>DD</sub>.
- Apply the LED voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V<sub>DD</sub> = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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## 7. PACKING

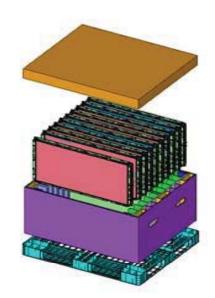
- 8.1 CARTON (Internal Package)
  - (1) Packing Form
    Corrugated fiberboard box and corrugated cardboard as shock absorber
  - (2) Packing Method

Packing -Pallet Box

**LCD Module** 

Packing -Pallet Box

**Pallet-Plastic** 



# 8.2 Packing Specification

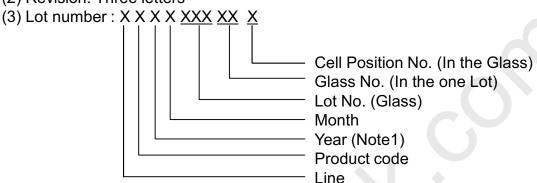
Item	Specification	Remark
LCD Packing	16 ea / (Packing-Pallet Box)	1. 10.5 kg / LCD (16ea) 2. 12 Kg / Cushion-pallet (2ea) 3. 8 Kg / Packing-Pallet Box (1ea) 4. Packing Material : Paper
Pallet	1Box / Pallet	1. Pallet weight = 8.8 kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1270mm(H) x 1150mm(V) x 844mm(height)
Total Pallet Weight	196.8kg	Pallet(8.8kg) + Module (10.5 * 16=168kg) + Cushion (up + bottom =12kg) + Pallet-BOX(8kg)

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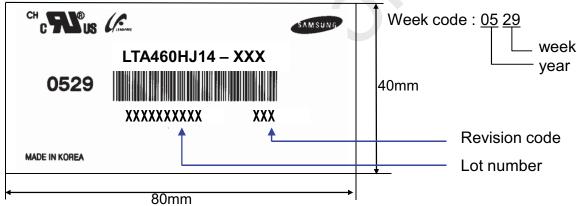
### 8. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

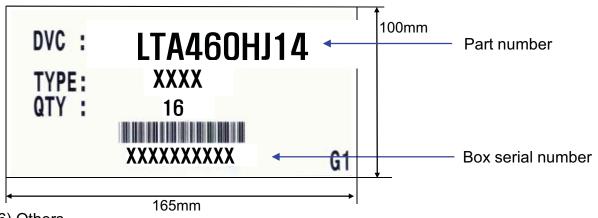
- (1) Part number: LTA460HJ14 XXX
- (2) Revision: Three letters



## (4) Nameplate Indication



#### (5) Packing box attach



- (6) Others
  - 1. After service part

LEDs cannot be replaced because of the narrow bezel structure.

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## 9. General Precautions

## 9.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the converter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and LED back light.
- (d) Note that polarizers are very fragile and could be damage easily.

  Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or Semiconductor would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not disassemble shield case of converter & LVDS board.
- (m) Do not connect N.C pins. (Samsung internal use only)
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handle a module
- (o) Pins of I/F connector should not be touched directly with bare hands.

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## 9.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 5 to 40  $^{\circ}$ C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.
- (d) Storage period is recommended not to excess 1 year.

#### 9.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its converter power supply should be connected directly with a minimized length. A longer cable between the back light and the converter may cause lower luminance of LED and may require higher startup voltage (Vs).

#### 9.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.Normal condition is defined as below;

Temperature : 20± 15 °CHumidity : 55± 20%

- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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#### 9.5 Others

Samsung Secret

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. ( supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)

  Otherwise the Module may be damaged.
  - Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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